

Abstract of the Disclosure:

A chip-type solid electrolytic capacitor comprises two capacitor elements using a valve metal and laminated in a direction perpendicular to a mounting surface to be mounted on a substrate. A pair of anode lead wires are extracted on one side from anode members of the capacitor elements in parallel to the mounting surface. The anode lead wires are connected to two branches of an anode terminal portion, respectively. A cathode terminal is connected to cathode layers on dielectric oxide films of the anode members. The solid electrolytic capacitor is encapsulated in an encapsulating resin with the anode terminal and the cathode terminal partially exposed. The branches of the anode terminal portion are configured so that they overlap each other by rotation of 180° around a center line.